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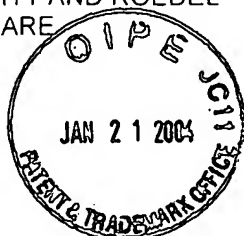
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/665,982	09/18/2003	1763	0.00	MEMC 02-0051 (3032.1)	16	129	8

000321

SENNIGER POWERS LEAVITT AND ROEDEL  
ONE METROPOLITAN SQUARE  
16TH FLOOR  
ST LOUIS, MO 63102



CONFIRMATION NO. 5374

## FILING RECEIPT



\*OC000000011482117\*

Date Mailed: 12/12/2003

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## Applicant(s)

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Correct Information:

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Mark G. Stinson, East Alton, IL;  
Guoqiang (David) Zhang, Ballwin, MO

## Assignment For Published Patent Application

MEMC Electronic Materials, Inc.; ✓

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/411,544 09/18/2002 ✓  
and claims benefit of 60/487,662 07/16/2003 ✓

## Foreign Applications

If Required, Foreign Filing License Granted: 12/11/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts ✓

Non-Publication Request: No

2/1/04

Early Publication Request: No

Title

Process for etching silicon wafers ✓

Preliminary Class

216

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